

## **Korusys Launch 4Kp60 & HDR capable, H.265 Contribution decoder at IBC2015.**

Southampton , UK – 11<sup>th</sup> September 2015

Korusys, Ltd announces the launch of the industry's best in class, ultra-low latency, 4.2.2. 12-bit, 4kp60, single chip decoder solution.

The Korusys HEVC IP core is integrated as a stand-alone, 1RU professional decoder and is being demonstrated at IBC 2015, showcasing live multi-channel decoding of multiple IP networked H.265 streams.

Come to stand 4.A61h where Korusys' staff will demonstrate the Korusys feature-rich GUI control system, real-time alarms, transport stream demultiplexing and exceptional bit-error concealment & correction.

Korusys are demonstrating to equipment makers how they can immediately go to market utilising the efficiency and flexibility of an FPGA core to support the rollout of both 4k & HDR in an H.265 contribution decoder product and to provide a future proof UHD contribution experience..

IBC show attendees can come and hear more about Korusys IPr solutions and capabilities showcasing our design services for Broadcast Solutions; providing a bespoke integration service to any conceivable network infrastructure or topology.

"Korusys is proud to unveil an industry-leading professional grade H.265 decode solution to the market" said Paul Rushton, Director of Marketing at Korusys. "Designed to augment and complement Altera's flexible encoder IPr which uses an FPGA, the Korusys decoder gives customers the benefits of Altera's FPGA flexibility, alongside a production ready H.265 IPr, for a full codec system solution."

"The Korusys H.265 decoder is an exceptionally high-quality solution - already proven through high volume industry deployments - that perfectly complements Altera's encoder to provide an ultra-low latency system solution for broadcast equipment makers," said Neal Forse, Senior Manager, Broadcast Business Unit, Altera.

### **About Korusys**

Korusys is a leading provider of FPGA Design Services and embedded product development and a member of the Altera Design Services Network. Korusys develops Intellectual Property (IP cores), and custom product solutions for customers in multiple markets including video compression technologies and real-time video processing techniques.

### **About Altera**

Altera® programmable solutions enable designers of electronic systems to rapidly and cost effectively innovate, differentiate and win in their markets. Altera offers **FPGA, SoC, CPLD**, and complementary technologies, such as **power solutions** to provide high-value solutions to customers worldwide. Visit Altera at [www.altera.com](http://www.altera.com).

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